## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Keisuke GOTO and Sachiko EDO

Appln. No.:

To be assigned

Art Unit:

To be assigned

April 4, 2001

Examiner:

To be assigned

Filed:

For:

DLL CIRCUIT AND METHOD OF

GENERATING TIMING SIGNALS

Docket No.:

KPM-01501

Certificate of Express Mailing

I hereby certify that the foregoing documents are being deposited with the United States Postal Service as express mail, postage prepaid, in an envelope addressed to the Commissioner for Patents Washington, D.C. 20231 on this date of April 4, 2001.

Express Mail Label No.: EL506928154US

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicants and/or Sir: their attorney in compliance with the requirements of 37 C.F.R. 1.56. Copies of the documents are also being submitted.

Since this application is being filed on even date herewith, no fee is due. In addition, in accordance with 37 C.F.R. 1.98, Reference AL listed on the Form PTO-1449 is a Japanese-language reference which is discussed in the present application.

The Examiner is respectfully requested to initial the space adjacent to each document on the PTO-1449 form and return a copy of the PTO-1449 form to confirm that these documents have been considered by the Examiner and made of record in this application.

Although we believe that we have appropriately provided for any fees due in connection with this submission, the Commissioner is authorized to credit any overpayment or charge any deficiencies to/from our **Deposit Account No. 501136**. Two originally-executed copies of this form are being submitted.

Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at (617) 951-6676.

Respectfully submitted,

HUTCHINS, X

April 4, 2001

Date

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